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ABSTRACT

A standardized or partial standardized circuit board core comprises at least a dielectric core layer and a plurality of conductive posts, in which the dielectric layer has a first surface and a related second surface. The conductive posts pass through the dielectric core layer and connect to the first and second surfaces of the dielectric layer respectively. The conductive posts are array arranged or arranged in a constant distance form in the dielectric core layer. Moreover, the standardized or partial standardized circuit board core further includes two conductive layers, which are covered on the first and second surfaces of the dielectric core layer.